## PATENT APPLICATION

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FR	1Q	Cristiano Perei Computing (20	ra et al., "A Power-i 02), pp. 155-168	Awar	e API For Embedded	and I	Portable Systems*, Power Awa	are	
FL	1R	Keith I. Farkas et al., "Policies For Dynamic Clock Scheduling", OSDI (October 2000, San Diego, California), pp. 43-51							
FR	18	Tajana Simunic et al., "Event-Driven Power Management", IEEE Transactions on CAD (July 2001), pp. 840-857							
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FR	1Q	Andrea Acquav HPL-2003-81 (	iva et al., *LP-ECO April 17, 2003)	S: An	Energy Efficient RTC	)S*, I	HP Laboratories Tec	hnical Repo	rt	
FR	1	Tajana Simunic pp. 102-125	, * Dynamic Manag	ement	of Power Consumpt	ion°,	Power Aware Com	puting (200	2),	
FR	18	Andrea Acquav Multimedia", IE	iva et al., "Softwa EE Transactions of	re Cor CAD	ntrolled Processor Sp (November 2001), p	eed \$	Setting For Low Pow 83-1292	er Streamin	eg	
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